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Application No. 10/646988 (Docket: CNTR.2209)  
37 CFR 1.111 Amendment dated 08/22/2007  
Reply to Office Action of 05/23/2007

**AMENDMENTS TO THE ABSTRACT**

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

**ABSTRACT OF THE DISCLOSURE**

A microprocessor apparatus An apparatus within a microprocessor is provided including multiple functional units, some of which can enter a power reduction mode to decrease overall power consumption when needed. The functional units generate respective activity signals to indicate the level of activity of each of the functional units. The activity outputs are monitored by utilization assessment logic to determine a current total power consumption value for the microprocessor. The microprocessor is capable of successively entering a series of power reduction modes when the current total power consumption value is greater than a threshold power value of a specified power profile where the specified power profile is selected from a plurality of power profiles that are stored within power control logic in the microprocessor, and where a select signal directs the power control logic to select the specified power profile.